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## **ABSTRACT OF THE DISCLOSURE**

A flat panel display includes a faceplate, a backplate combined with the faceplate to form a vacuum tight cell, and a light emission unit placed within the cell to emit light from the cell. The backplate has a plurality of electron emission sources. A frame is mounted on the backplate with opening portions. The electron emission sources are exposed through the opening portions of the frame toward the faceplate. A plurality of spacers are formed on the frame such that the spacers are positioned at a non-display area within the cell. A plurality of gate electrodes are formed at a surface of the frame with a predetermined pattern. The gate electrodes has opening portions communicating with the opening portions of the frame.